

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. TANAKA, et al.
Application No.: 10/586,305
Filed: JULY 14, 2006
For: MANUFACTURING METHOD OF A SEMICONDUCTOR
DEVICE HAVING ELECTRODES ON OPPOSED SIDES OF AN
IC ELEMENT THEREOF AND FIRST AND SECOND CIRCUIT
LAYERS ELECTRICALLY CONNECTED TO THE
ELECTRODES
Group: 2829
Examiner: Shaun M. Campbell
Confirm. No.: 2516

SUBMISSION UNDER 37 CFR 1.114
(AMENDMENT)

Mail Stop: RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

April 20, 2009

SIR:

In response to the Office Action mailed November 20, 2008, and concurrently with the filing of a Request for Continued Examination (RCE) Transmittal, the period for response having been extended for two (2) months by the attached Petition for Extension of Time, please amend the above-identified application as listed in the following, and as set forth on the following pages:

AMENDMENTS TO THE SPECIFICATION;

AMENDMENTS TO THE CLAIMS; and

REMARKS are included following the amendments.